

PATENT NUMBER

100-443880-100	09/73555	12/14/88
Class	Subclass	ISSUE CLASSIFICATION

O.I.P.E.

PATENT DATE

12W O.I.P.E.
SCANNED 22 ① Q.A. AA

APPLICATION NO. 09/735555	CONT/PRIOR F	CLASS 740 326	SUBCLASS 87	ART UNIT 2181 2819	EXAMINER D. CHANG
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APPLICANTS

Output buffer circuit and control method therefor

TITLE

PTO-2040
12/89

ISSUING CLASSIFICATION												
ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION												

☐ Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED	
	<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____			
ISSUE FEE				
	_____ (Primary Examiner) (Date)		Amount Due	Date Paid
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		ISSUE BATCH NUMBER	

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